

Q: What is the Symbolic Addressing Mode?

Symbolic Addressing in Program Mode

The vector comment field can be employed for symbolic addressing. Certain rules are enforced, primarily to extend program mode to QuickLoad in AutoTest. This beta feature is invoked by the SpScript keyword AllowSymbolicAddressInCommentsø The following Rules & Regulations apply:

- A. The first comment line (vector 0 comment) must start with the keyword ÷_PrgCmdø indicating that comments have program commands.
- B. First line of program must be PAGE 0, and second line of program must be JMP 2. Note: these program commands are automatically generated (the aforementioned keyword does the invocation).
- D. Last vector comment indicates last vector by the keyword ÷_LastVecøó inserted automatically. This, however, is not a label; just a comment convenient when the feature of comment search is used. Similarly, the StopVector will have the comment ÷_StopVectorøinserted. (CommentSearch makes these comments an operating convenience).
- E. The comment for the last vector minus 1 must begin with ':END'. This label is also generated automatically. Between

-BEGINøand -ENDø(inclusive) is the program space accessible as references by the various branch instructions of the program.

By these five simple rules, of which conformation and/or generation is automatic, lies the foundation for applying program mode to QuickLoad in AutoTest. In addition, symbolic addressing must be adhered to throughout. While we can run vector programs where symbolic programming is only adhered to partially, such programs cannot be applied to AutoTest if QuickLoad is desired.

The syntax for the program itself is simple and summarized by the following rules:

- A. Only branch instructions associated with labels (JMP, CJMP, CALL, CCALL) need to be considered for programming in the comment field. The LOOP instruction has by our new convention no label associated with it. It is called LOOP@, indicating looping to itself. If the user desires the old convention, we have provided equivalency by invoking the keyword -UseOldProgramRulesø
- B. To assign a symbolic label to a branch instruction, just type a ∴\$øfollowed by ≔ø, ∴+ø, ÷-;, or LabelName. The ≔øsignifies branch to itself (e.g. waiting for a condition), while ÷+ø and ÷-; signifies relative addressing that is, relative to the location of the branch instruction (the +/- will be followed by an integer which determines the relative branch).

LabelName is any label of any size, of which the first 15 characters have significance (i.e. 16th character and characters beyond are ignored).

C. The LabelName associated with a vector must appear in the comment field at that vector and be preceded by the delimiter ∴ø The label name itself could be any alphanumeric string of which the first fifteen characters have significance.

The vector display to the right illustrates the concept.

Note: the line with \div _StopVecø indicating the Stop Vector, which is not the same as Last Vector. ETS2k warns the user of this deviation from the rules, but allows it to run anyway. The warning message will only appear one time during the entire session.

This comment file should be saved as a .PRA file when uploading vectors.

Also See:

Q'nApp #E7: Vector Looping Q'nApp #E8: Pattern Matching Q'nApp #E15: Special Script Q'nApp #E33: MASK and UNMSK Q'nApp #E39: PRG file attachment Q'nApp #E44: Vector files w/o path

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Before pressing Run:								
Vector Address (Hex)	 Program 	 Split	Cycle	 Comments 				
00000 00001 00002 00003 00004 00005 00006 00007 00008 00009 00000A 00000B 0000C 0000D 0000C 0000F 0000F 00010	LOADX 00 LOAD 0FFF NOOP NOOP CJMP 0000 JMP 0000 NOOP NOOP NOOP LOOP@ NOOP NOOP	00000005 00000006 00000007 00000008 00000008 00000008 00000008 00000008 00000008 00000008 00000008 00000008 0000008 0000008 0000008 0000008 0000008 0000008 0000008 0000008 0000008	0000001 0000002 0000003 0000004 0000005 0000006 0000008 0000008 0000008 0000008 0000008 000000	<pre> \$Alabel more comment \$+3;jump to adr +3 </pre>				
After pressing the Run button, the display changes to:								
Vector Address (Hex)	 Program 	 Split	Cycle	 Comments 				

(IIEX)	İ			Ì	Split	Cycle	
00000 00001 00002 00003 00004 00005 00006 00007 00008		PAGE JMP LOADX LOAD NOOP NOOP CJMP JMP	00 0002 00 0FFF 000A 000B		00000000 00000002 00000002 00000003 00000004 00000005 00000006 00000006 00000007 00000008	00000000 00000001 00000002 00000003 00000004 00000005 00000006 00000006 00000007 00000008	<pre></pre>
00009 0000A 0000B 0000C 0000D 0000E 0000F 0000F		NOOP NOOP LOOP@ NOOP NOOP NOOP			0000008 0000008 0000008 0000008 0000008 000000	0000008 0000008 0000008 0000008 0000008 000000	:Alabel _StopVec :END _LastVec

Importantly, this real time compilation (perhaps more correctly assembly) slows down the running process as a function of several factors, primarily the number of lines of comments and the total number of comments. While, the translation will only occur as required (a subsequent pressing of the RUN button will skip the compilation process), it slows down the run when a comment is added or changed. We understand the essence of the element of time during the debugging process, and have done our utmost to mitigate this timing predicament. However, assembly is never done instantly and the price to pay may warrant temporary deviations from the norm. Do this by changing the aforementioned keyword - PrgCmdøto - PrgCmdø and the software will behave as if no compilation existed. Then, when the job nears completion, you can reintroduce ÷_PrgCmdøand finish off the job with a program well documented and easily enhanced and modified.