

Q: What is Bit-map and how do I use it?

Presto BIT-MAP

The bit-map feature of Presto locates failures on a memory device by generating either a logical or physical map of the device. Using the bit-map, two different maps can be created: a general map or a detailed map. A general map contains information about addresses, and a detailed map shows actual data failures. The general map displays one red spot for every address (every word). Detailed maps display a red spot for every bit, so the bitmap will be 8 times wider. Before performing any bitmapping, it is assumed that a valid setfile and vector file have been loaded, and that the memory test parameters have already been set in the Memory Test window.

For use of bitmap and permutations discussed in this document, the following Special Script must be enabled in the SpScript.txt file found in the Presto folder (remove the leading semicolon to enable the special script):

MemTestNewRowColWay



Physical Representation

In order to display the physical representation of a device (i.e. a memory chip) as a bitmap, two different tools are available:

1. Scramble address bits. Define a permutation of address bits, according your specification of physical locations on the chip. Note that after this operation, all 8 bits (from a single location) will remain grouped together for display purposes.

create bitmaps.

2. Split data bits. Create up to 8 group of bits, for generating a õrealö bit map of the device. For every row (page), the bits will be displayed according to these groups. For example:

for 8 data bits, define 4 groups: 0 1, 2 3, 7 6, 5 4.

In this case, the representation (for a detailed map) will display like this:

d0d1d0d1í í d0d1d2d3d2d3í í d2d3d7d6d7d6í .d7d6d5d4í d5d4 (i.e., data zero, data one, data zero, data oneí) Every group is repeating ÷columnøtimes. Number of columns = page size.

Setting Scramble and Group Information

To set scramble and group information, go to the Memory Address Mapping window (from FA sidebar choose Memory FA). The window will use the Memory Test õWidthö value as the number of pins.

The left side of the window is used to define the permutation of address bits. The row and column information is imported from the Memory Test window.

The right side is used to define the bit groups. :

- add a new bit to a group by selecting the bit in the list and clicking on the group.
- delete a bit from a group by pressing SHIFT and clicking on the group. The last bit in the group is moved to the list.

See the following example.



Working with the Memory FA Window

Use the TEST button to check the permutation of address pins. All the information about scramble and groups can be saved in õ.grpö files by clicking the Save button. This is an ordinary ASCII text file, but the information seen in this file is of value only to the Presto software and is used only for saving and restoring the permutation definitions. (Do not edit this file.)

By loading the file õtest.grpö, our group permutations now look like this:

Execute Test	Row Map	🗖 Column Map ——	Groups info ————————————————————————————————————
Execute	R bit 1 -> 6 ++++++++++++++++++++++++++++++++++	C bit 1 -> 0	1 2,1 2 4,3
Analyze	$4 \rightarrow 9 \rightarrow 10 \rightarrow 10 \rightarrow 10 \rightarrow 10 \rightarrow 11 \rightarrow 12 \rightarrow 12 \rightarrow 12$	4 -> 3 ++ 5 -> 4 ++ 6 -> 5 +	3 7,8 4 5,6
Bitmap Load Save Display F Bit Display	8 -> 13 . 9 -> 14 <u>.</u>		Groups 4
	Test	Default	Cancel OK

When OK is clicked in the scramble window, all information is loaded and used until the program is finished, or until another õgrpö file is loaded. This also closes the window and returns to the Memory Test window for execution and bitmapping.

Creating the Bit-map

Test the device by clicking the õExecuteö button in the Memory Test window. Press the õAnalyzeö button in order to generate data for the bit-map. This will create and open an ASCII file called õfailures.logö, which could be large and may take some time to generate. It is important to remember that each bitmap is created from the *last* õAnalyzeö event. Hereøs an example of a failures.log file:

Failures for C:\ets\Memchip.set				
Address	Expecting	Actual		
002D40	00000000	10001001		
002D41	11111111	10101001		
002D42	00000000	10101001		
002D43	11111111	10101001		
002D44	00000000	10101001		
002D45	11111111	10101001		
002D46	00000000	10101001		
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Partial failures.log file

Failures.log is always saved in the Presto root directory, and is overwritten by each õAnalyzeö event. Clicking the BitMap button will display a graphic image using the data in this file (it does not matter if the file is open or closed). An example of a bitmap image using the example failures.log file (without the use of the õBit infoö option) appears below.



Bitmap of "Failures.log"

Navigate through errors by pressing $\pm \phi$ (first), $\pm p\phi$ (previous), $\pm n\phi$ (next) and $\pm \phi$ (last) keys, or by clicking on the <u>First</u>, <u>Prev</u>, <u>Next</u> and <u>Last</u> buttons. The mouse cursor will automatically move to the failure, and the window will also display specific Address and Bit information. In our example, the cursor is pointing to address 002D40 (hex), and the eight bits at this address are represented as õfpppfppfö (where f = failed and p = passed). Click inside the yellow square and the cursor will move to the next failure according the current mouse position.

Use of the Bit info feature will generate a highly detailed bitmap as seen on the last page. It is not necessary to Execute and Analyze again if it has been done once prior to checking the Bit info box.



Bitmap using the "Bit info" feature

Also See: Q'nApp #P4: Memory Test Q'nApp #P36: Memory Test Functional Description