## Versatile, Flexible AC/DC Test System

Everyone wants to reduce the cost of test. HILEVEL has gone a step beyond, by defining the future of test with the Griffin III Hybrid system. We call it "Hybrid" simply because it combines the most versatile offerings of features in a single system, at the lowest cost. For devices requiring DC and Continuity test capability only, G3H is a very flexible cost-effective Production Test solution. This approach provides a DC test system with the capability to add logic resources (AC/SCAN) as well as analog resources for Mixed Signal. The HILEVEL Griffin III Hybrid embraces low cost while supporting SCAN, giving you the ability to toggle every node in your chip. Contact HILEVEL today, and start getting serious "mileage" from your tester.


## GRIFFIN111



## DC Test

## Price and Performance



The flexibility of a HILEVEL system brings new price/performance efficiency to the Tester-in-aHead tradition, a concept created and introduced by HILEVEL in 1987. This tester is a superior cost-effective solution for Mass Production applications. HILEVEL's own proprietary tester ASICs provide the power and versatility so crucial in a Production Test system.

## Multi-Site

Each DC PEB card provides DC test for 128 pins. The HILEVEL G3H chassis has slots for 8 DC boards (Up to 32 sites of 32 DC pins each) and four slots for standard PEB pin electronics boards for full AC, DC or SCAN testing (32 pins each). This standard configuration of the HILEVEL Griffin III Hybrid DC system can support up to 1,024 DC pins plus 128 AC/DC channels, but other configurations are available. Software assigns the desired number of sites among these boards; all pins in one site, or divide the pins into two sites, four sites, etc. The G3H system can provide up to 40 power supplies for Multi-site testing.


Rich in GUI functions for engineering, and all the capabilities that make a mass production tester fast, HILEVEL's new Symphony III software provides the tools for efficient test development.


## Analog Resources for

 Mixed-Signal TestingHiLevel's Mixed Signal resource is more flexible than ever with the MX2. The modular design allows you to configure the MX2 with the analog resources that best fit your application. Choose from 16-bit or 24-bit AWGs, and 16-bit and 24-bit digitizers.


## Direct Docking

The Compact G3H and low-cost manipulator make a great team for direct docking. Used with our precision J750-compatible pogo tower, HiLevel's Griffin III Hybrid can easily adapt to your direct docking test needs.


## GRIFFIN111  <br>  <br> Specifications

MAINFRAME
Maximum DC Pins (8 slots)
Up to 1,024 DC pins in increments of 128 pins (128 pins per slot).
Maximum sites: 32
Maximum AC Pins (4 slots)
Up to 128 AC pins in increments of 32 pins ( 32 pins per slot).
Maximum AC sites: 4
Other configurations are available.

## OTHER SLOTS

Optional Power Supplies (2 slots)
Accommodates one or two MPS optional DUT Power Supply boards.

MX2 Mixed Signal Option (2 slots)
Install one or two configurable MX2 boards for Analog resources.

FCB Fast Clock Board Option (1 slot)
System Basic Boards (3 slots)

## Griffin III Hybrid

Card Cage Configuration


## GRIFFIN1TIT  <br> Specifications

DC PIN RESOURCES
DC Drive Range: 0-5V
Resolution: 20 mV
Current: Sink/Source $\pm 10 \mathrm{~mA}$

All measurements referenced to DUT ground.

## DC PARAMETRIC MEASUREMENTS

One DCPMU per 32 pins ( 4 per DC card)
Force Voltage Range: -8 V to +8 V
Resolution: 1mV
Force Current Range: -150 mA to +150 mA
Resolution \& Accuracy: Range Dependent

Voltage Measurement Range: -8 V to +8 V
Accuracy: $0.2 \% \pm 2 \mathrm{mV}$
Current Measurement Range: $\pm 150 \mathrm{~mA}$
Resolution \& Accuracy: Range Dependent

| Current Ranges | Resolution | Accuracy |
| :--- | :--- | :--- |
| $\pm 200 \mathrm{nA}$ | 10 pA | $\pm 0.4 \%$ of Value +40 pA |
| $\pm 2 \mu \mathrm{~A}$ | 100 pA | $\pm 0.3 \%$ of Value +400 pA |
| $\pm 20 \mu \mathrm{~A}$ | 1 nA | $\pm 0.2 \%$ of Value +4 nA |
| $\pm 200 \mu \mathrm{~A}$ | 10 nA | $\pm 0.2 \%$ of Value +40 nA |
| $\pm 2 \mathrm{~mA}$ | 100 nA | $\pm 0.2 \%$ of Value +400 nA |
| $\pm 20 \mathrm{~mA}$ | $1 \mu \mathrm{~A}$ | $\pm 0.3 \%$ of Value $+4 \mu \mathrm{~A}$ |
| $\pm 150 \mathrm{~mA}$ | $10 \mu \mathrm{~A}$ | $\pm 0.4 \%$ of Value $+40 \mu \mathrm{~A}$ |

## FCB: HIGH-SPEED CLOCKS

Eight fast clocks per FCB board with complementary outputs, up to 500 MHz with programmable fractional ratio to the test rate (from 1:1 up to 8:1, in 0.5 steps). One FCB per system maximum.

WORKSTATION AND SOFTWARE
OS:
Windows 7
Automation: ACT (Automation C Tools), TexTest for ASCII test control, or HILEVEL AutoTest (GUI)
Controller: PC workstation, Windows 7, and HILEVEL Symphony III software Interface: USB 2

# GRIFINITII DUSM 

## Specifications

## TEST RATE (AC pins)

Max Data \& Compare Rate: $\quad 200 \mathrm{MHz}$
Max Cycle Rate: $\quad 100 \mathrm{MHz}$, all modes (Two compares per cycle; two level transitions per cycle)
Resolution:
$125 \mathrm{KHz} / 1 \mathrm{MHz}$
Accuracy: $\quad 0.1 \%$ of programmed value
Minimum test rate: $\quad 125 \mathrm{KHz}$
AC TIMING
Timing Generators: 32, Globally Assigned
Time Sets on the fly: 16 programmable timing/format sets on the fly (switched dynamically during test) 128 timing sets total
Range: $\quad$ Entire clock cycle +10 ns
Resolution: 50ps
EPA (Edge Placement Accuracy): Standard Calibration: $\pm 1.5 \mathrm{~ns}$
Precision Calibration: $\pm 500 \mathrm{ps}$

## PROGRAMMABLE PATTERN GENERATOR

Program Commands: Jump, Conditional Jump, Call, Conditional Call (four levels of Nesting), Return, Conditional Return, Loop (Repeat), Page ( 16 bit pages), Set Counter Value, Decrement Counter, Clear Fail
Status, Trace mask On / Off, Pattern Match function.

## DATA FORMATS

NRZ Non-return to zero
R0 Return to zero
R1 Return to one
RI Return to inhibit
RC Return to compliment
(Surround by compliment)

## AC PIN ELECTRONICS (32 channels per siot)

Logic Pins: DRIVERS
All pins Input or Output or Bi-directional Min/Max Channels: 32/512
Increments of: 32
Pin To Pin Skew: +/- 500ps
VIH: (VIL +100 mV ) to +6.5 V
VIL: -1.5 V to (VIH -100 mV )
Resolution: 5 mV
Rails: 1 pair per pin
Accuracy: +/-10mV
Sink/Source Current: $50 \mathrm{~mA} / 50 \mathrm{~mA}$
Slew Rate: $1.5 \mathrm{~V} / \mathrm{ns}$
Capacitance: (Lumped + Continuous) $<50 \mathrm{pF}$

Logic Pins: RECEIVERS/COMPARATORS
Range: -1.5 V to +6.5
Resolution: 5 mV
Rails: 1 per pin/per threshold
Accuracy: +/-15mV
PE Memory:
Vector Depth: 64M per pin
Acquisition Depth: 64M in Sequential mode 16 M in Programmed mode
Scan (Optional):
Scan depth: Up to 8Gbit
Up to 128 scan chains
Full scan capture capability up to 64 M

## Specifications



## Mixed Signal Option

The G3H system provides two slots for optional MX2 analog resource boards. The MX2 consist of a master board that accommodates up to four submodules. These submodules can be mixed or matched in any combination on the MX2 master board. The submodules currently available are described below.

16-Bit Fast AWG
Resolution: 16-bit
Update rate: 80MSPS
Pattern depth: 1M
Output ranges: $0.75 \mathrm{~V}, 1.5 \mathrm{~V}, 2 \mathrm{~V}, 3 \mathrm{~V}$, $4 \mathrm{~V}, 6 \mathrm{~V}, 8 \mathrm{~V}, 12 \mathrm{~V}$
Output offset voltage: -3 V to +3 V
Output filters: none, $10 \mathrm{MHz}, 25 \mathrm{MHz}$
DNL $\leq \pm 0.5$ LSB @ $+25^{\circ} \mathrm{C}$
INL $\leq \pm 1.0$ LSB @ $+25^{\circ} \mathrm{C}$
THD $\leq-95 \mathrm{~dB} @$ fOUT $=1 \mathrm{MHz}$
SFDR $\geq 78 \mathrm{dBc}$ @ fOUT $=20 \mathrm{MHz}$

## 24-Bit Precision AWG

Resolution: 24-bit
Update rate: 196kSPS
Pattern depth: 1M
Output ranges: $0.75 \mathrm{~V}, 1.5 \mathrm{~V}, 2 \mathrm{~V}$, $3 \mathrm{~V}, 4 \mathrm{~V}, 6 \mathrm{~V}, 8 \mathrm{~V}, 12 \mathrm{~V}$
Output offset voltage: -3 V to +3 V
Output filters: none, $1.5 \mathrm{kHz}, 22 \mathrm{kHz}, 100 \mathrm{kHz}$
SNR/DNR $\geq 120 \mathrm{~dB}$
$\mathrm{THD}+\mathrm{N} \leq-110 \mathrm{~dB}$

## 16-Bit Digitizer

Resolution: 16-bit
Update rate: 80MSPS
Pattern depth: 1M
Input ranges: $0.75 \mathrm{~V}, 1.5 \mathrm{~V}, 2 \mathrm{~V}, 3 \mathrm{~V}$, $4 \mathrm{~V}, 6 \mathrm{~V}, 8 \mathrm{~V}, 12 \mathrm{~V}$
DC offset voltage: -3 V to +3 V
Input filters: none, $1 \mathrm{MHz}, 10 \mathrm{MHz}, 25 \mathrm{MHz}$ Input impedance: 1MOhm, or 600 Ohm
DNL $\leq \pm 0.5$ LSB @ $+25^{\circ} \mathrm{C}$
INL $\leq \pm 3.0$ LSB @ $+25^{\circ} \mathrm{C}$
$\mathrm{S} / \mathrm{N} \geq 77 \mathrm{~dB} @ \mathrm{fOUT}=10 \mathrm{MHz}$
SINAD $\geq 75 \mathrm{~dB} @ \mathrm{fOUT}=10 \mathrm{MHz}$
SFDR $\geq 80 \mathrm{dBc}$ @ fOUT = 10 Mhz

## 24-Bit Digitizer

Resolution: 24-bit
Update rate: 2.5MSPS
Pattern depth: 1 M
Input ranges: $0.75 \mathrm{~V}, 1.5 \mathrm{~V}, 2 \mathrm{~V}$, $3 \mathrm{~V}, 4 \mathrm{~V}, 6 \mathrm{~V}, 8 \mathrm{~V}, 12 \mathrm{~V}$
DC offset voltage: -3 V to +3 V
Input filters: none, programmable from
19.2 kHz up to $1.35 \mathrm{MHz}, 16$ - steps Input impedance: 1MOhm, or 600 Ohm DNL - guaranteed monotonic to 24 bits
INL $\leq 0.00076 \%$ FS
$\mathrm{S} / \mathrm{N} \geq 112 \mathrm{~dB}$
THD $\leq-105 \mathrm{~dB}$
SFDR $\geq 120 \mathrm{dBc}$

## Specifications

## MAIN DUT POWER SUPPLIES

## PS1

Range: 0 to $8 \mathrm{~V}, 0$ to 2 A
Resolution: $5 \mathrm{mV}, 10 \mathrm{~mA}$
Accuracy: +/-15mV
PS2 \& PS3
Range: +/-16V, 0 to 1A
Resolution: $5 \mathrm{mV}, 10 \mathrm{~mA}$
Accuracy: $+/-15 \mathrm{mV}$
PS4
Range: 0 to $3.7 \mathrm{~V}, 0$ to 4 A
Resolution: 5 mV , 10 mA
Accuracy: +/-10mV

## Current Measurement Resolution

Range dependent.
Best resolution is 1 nA .

Optional Multiple DUT Supply Boards
Four slots dedicated to accommodate up to four DUT supply boards in any
combination:
MPS1
One 0-3.7V/4A, One 0-8V/2A, Two $\pm 16 \mathrm{~V} / 1 \mathrm{~A}$
MPS2
Four 0-3.7V/4A
All with Alternating Voltage Source (AVS)
Multi-Site Supplies
One Supply per each 32 Pins (site)
Voltage Range: $0-8 \mathrm{~V}$
Resolution: 5 mV
Accuracy: 15 mV
Current Range: 0-1A
Resolution: 10 mA
Accuracy: see table below

## DUT SUPPLY MEASUREMENT RANGES

One Measurement Unit per system
Voltage Measurement Range: -16 V to +16 V , Resolution: 5 mV , Accuracy: $0.2 \% \pm 2 \mathrm{mV}$

| Current Ranges | Resolution | Accuracy |
| :--- | :--- | :--- |
| $\pm 200 \mathrm{nA}$ | 10 pA | $\pm 0.5 \%$ of Value +100 pA |
| $\pm 2 \mu \mathrm{~A}$ | 100 pA | $\pm 0.4 \%$ of Value +500 pA |
| $\pm 20 \mu \mathrm{~A}$ | 1 nA | $\pm 0.2 \%$ of Value +4 nA |
| $\pm 200 \mu \mathrm{~A}$ | 10 nA | $\pm 0.2 \%$ of Value +40 nA |
| $\pm 2 \mathrm{~mA}$ | 100 nA | $\pm 0.2 \%$ of Value +400 nA |
| $\pm 20 \mathrm{~mA}$ | $1 \mu \mathrm{~A}$ | $\pm 0.4 \%$ of Value $+4 \mu \mathrm{~A}$ |
| $\pm 200 \mathrm{~mA}$ | $10 \mu \mathrm{~A}$ | $\pm 0.4 \%$ of Value $+40 \mu \mathrm{~A}$ |
| $\pm 2 \mathrm{~A}$ | $100 \mu \mathrm{~A}$ | $\pm 0.5 \%$ of Value $+400 \mu \mathrm{~A}$ |
| $\pm 5 \mathrm{~A}$ | $250 \mu \mathrm{~A}$ | $\pm 0.5 \%$ of Value +1 mA |

OTHER FEATURES<br>Real time Failure Counter<br>Shows number of fails while running<br>Display Capture Fails Only<br>Acquisition Memory Compression<br>High-speed Acquisition Search:<br>Search 64M of capture in $<3$ sec.<br>Full "Next Cycle" Operation<br>Data Valid for the full next cycle

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ENVIRONMENTAL
Power
220VAC single phase, Max 20A
Max Weight
85kg approx. (16 cards installed)
Manipulator Adapter Option: 10kg
Dimensions (Test head only)
H508mm x W438mm x D438mm
Cooling
9 fans
Temperature
60 to }80\mp@subsup{}{}{\circ}\textrm{F}(16\mathrm{ to }2\mp@subsup{7}{}{\circ}\textrm{C}
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